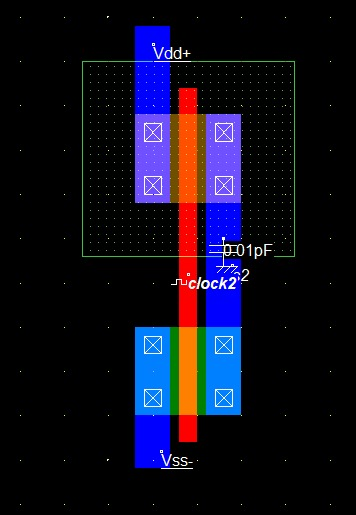
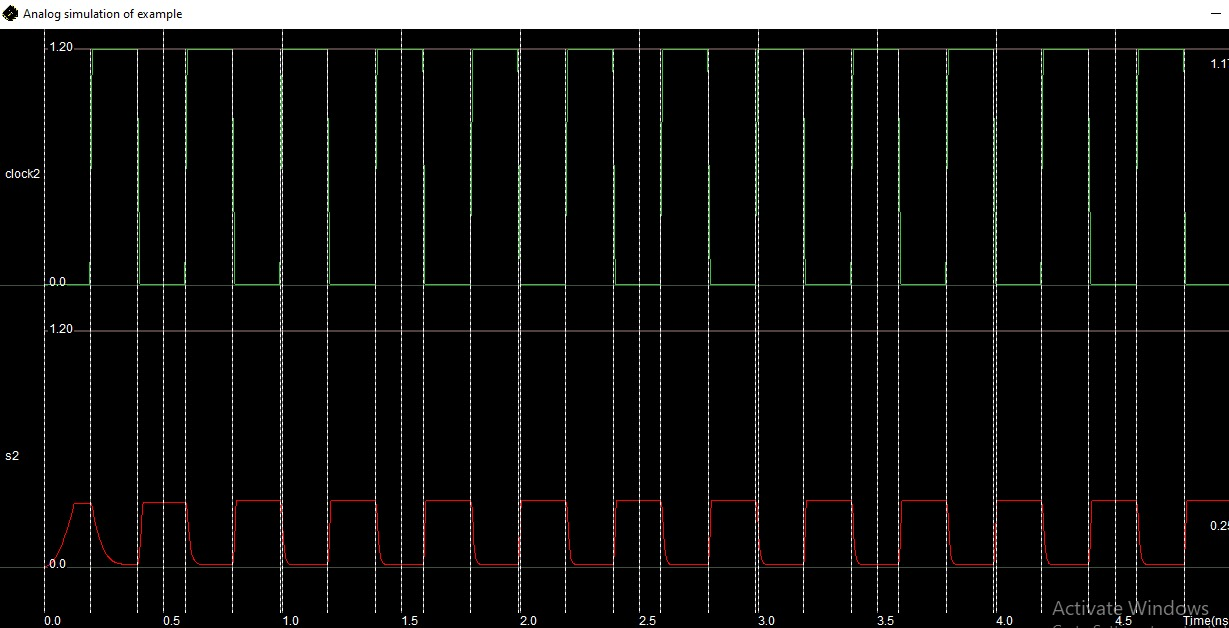
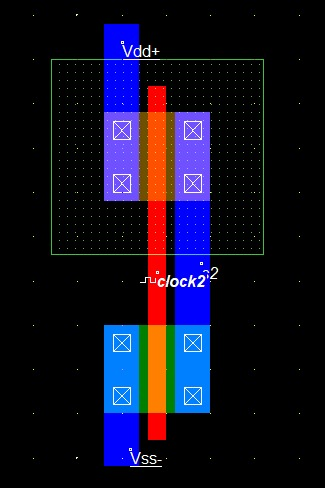
**Inverter with Capacitor :**

****

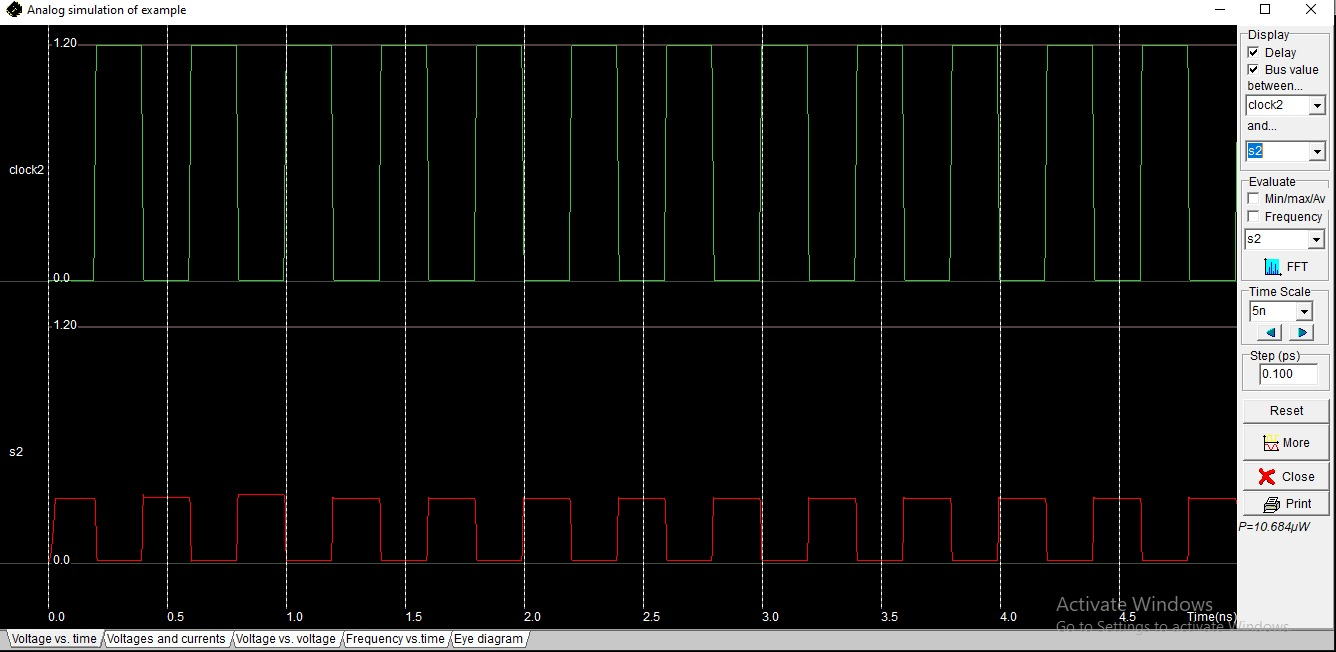
**Output :**

****

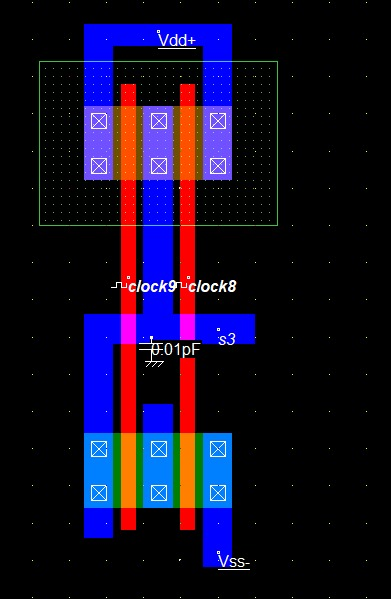
**Inverter without Capacitor :**

****

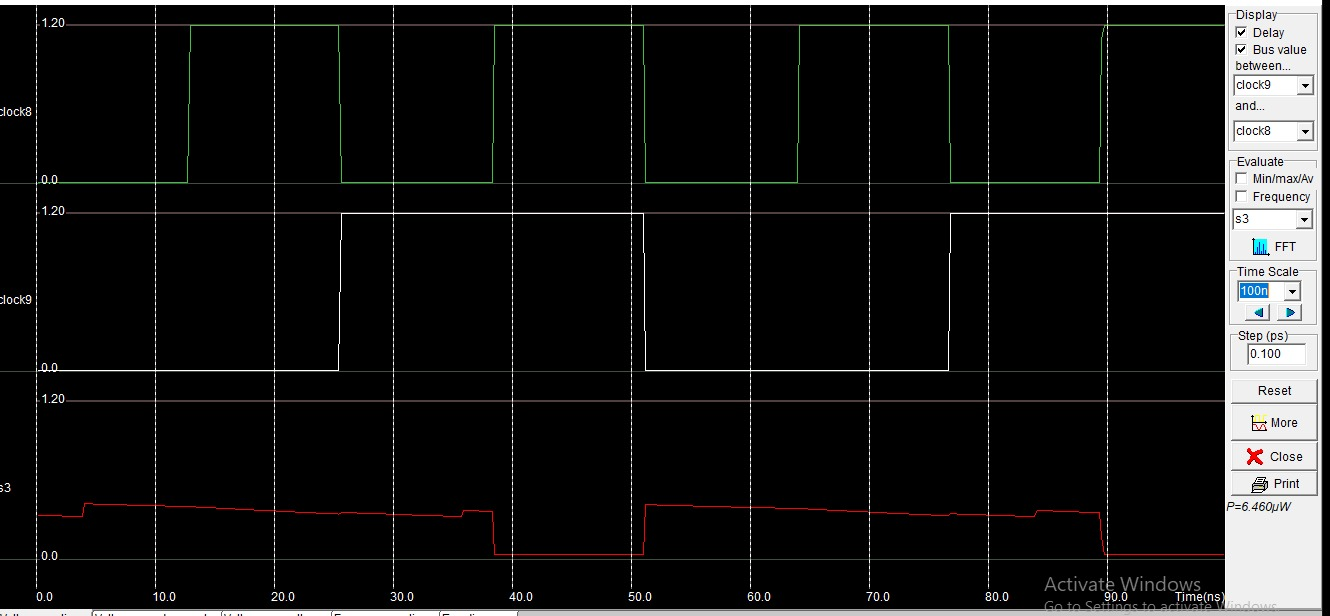
**Output :**

****

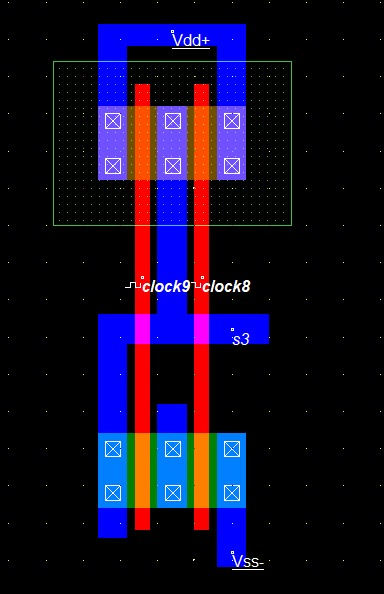
**NAND with Capacitor :**

****

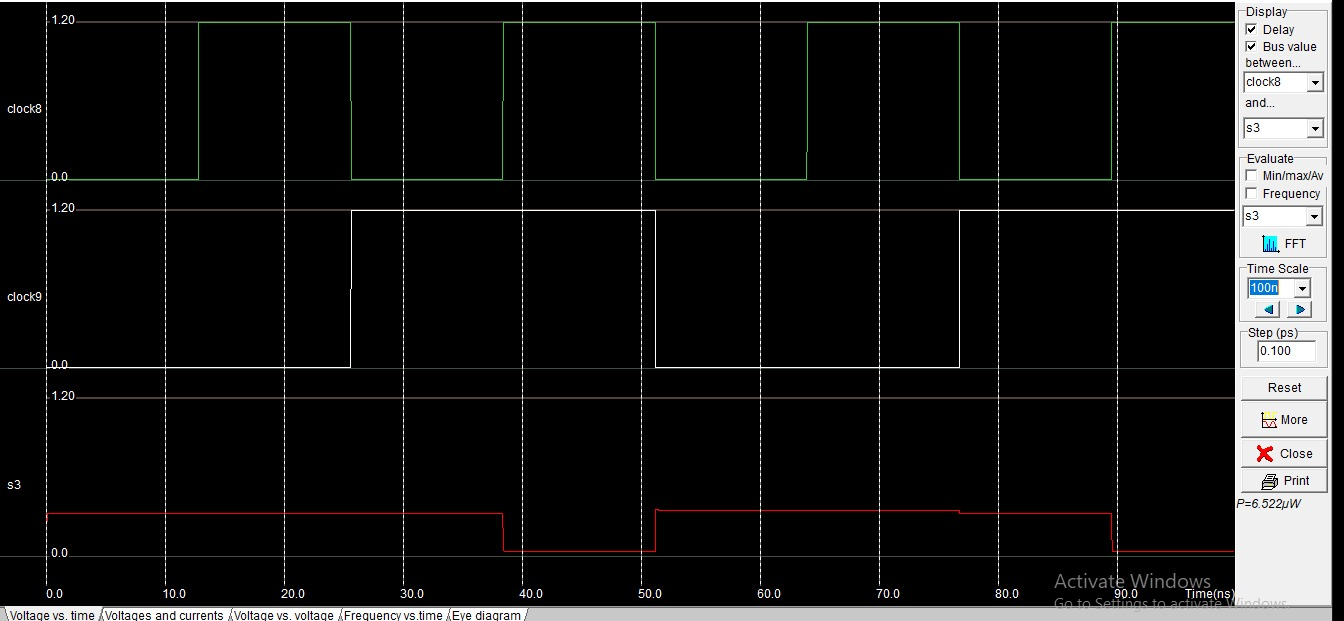
**Output :**

****

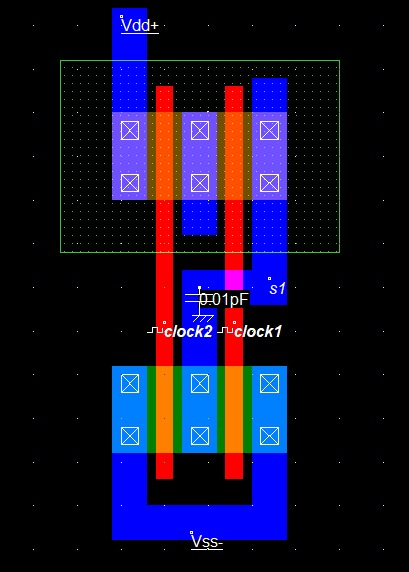
**NAND without Capacitor :**

****

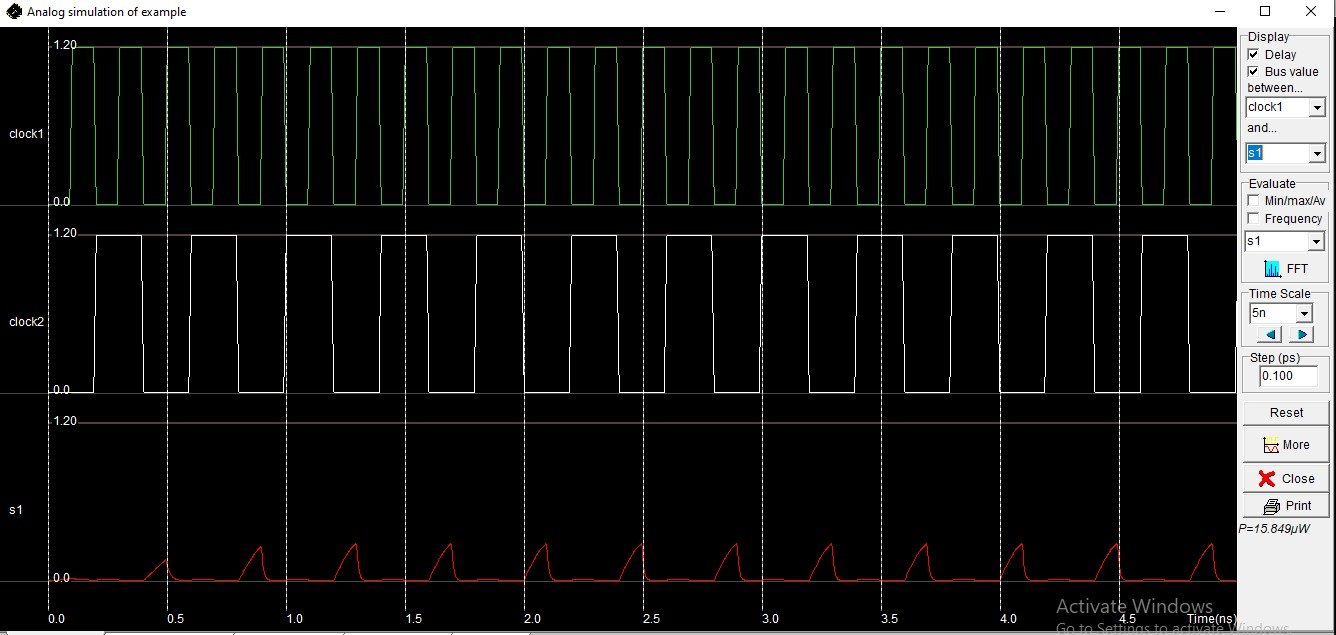
**Output :**

****

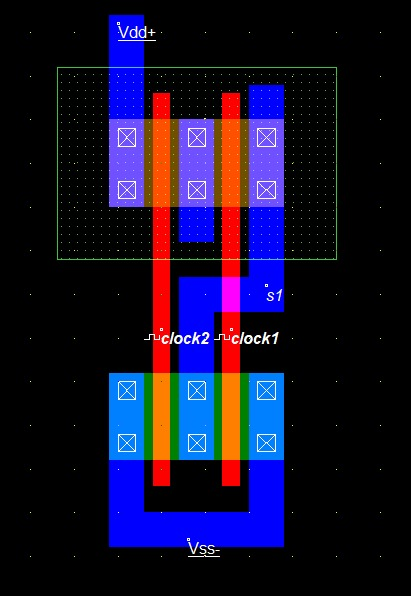
**NOR with Capacitor :**

****

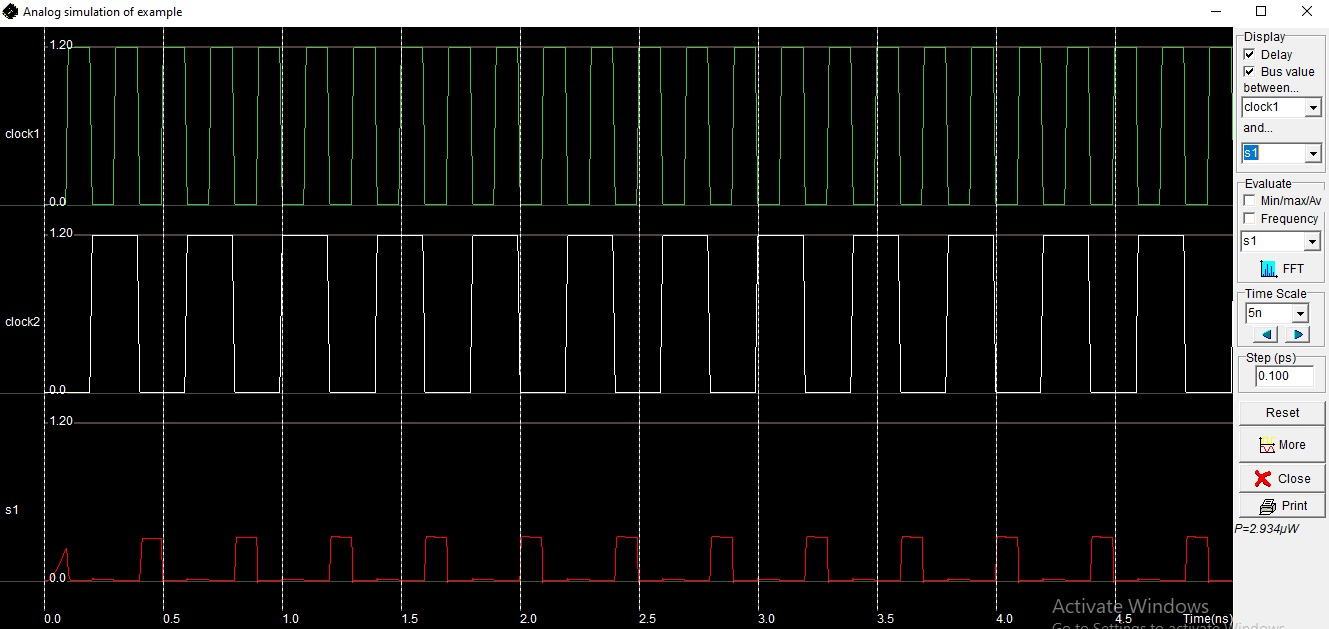
**Output :**

****

**NOR without Capacitor :**

****

**Output :**

****